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## Patent Assignment Abstract of Title

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## Total Assignments: 1

Patent #: NONE

Issue Dt:

Application #: 10830042

Filing Dt: 04/23/2004

Publication #: 20050240737

Pub Dt: 10/27/2005

Inventor: John M. Holt

Title: Modified computer architecture

## Assignment: 1

Reel/Frame: 014994/0105

Recorded: 08/09/2004

Pages: 4

Conveyance: ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS).

Assignor: HOLT, JOHN M.

Exec Dt: 07/28/2004

Assignee: WARATEK (AUSTRALIA) PTY LIMITED

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Correspondent: CONNOLLY BOVE LODGE &amp; HUTZ LLP

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## Total Assignments: 1

Patent #: NONE

Issue Dt:

Application #: 11111757

Filing Dt: 04/22/2005

Publication #: 20050257219

Pub Dt: 11/17/2005

Inventor: John M. Holt

Title: Multiple computer architecture with replicated memory fields

## Assignment: 1

Reel/Frame: 016608/0543

Recorded: 08/03/2005

Pages: 4

Conveyance: ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS).

Assignor: HOLT, JOHN M.

Exec Dt: 07/25/2005

Assignee: WARATEK PTY LIMITED

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## Total Assignments: 1

Patent #: NONE

Issue Dt:

Application #: 11111778

Filing Dt: 04/22/2005

Publication #: 20060095483

Pub Dt: 05/04/2006

Inventor: John M. Holt

Title: Modified computer architecture with finalization of objects

## Assignment: 1

Reel/Frame: 016608/0629

Recorded: 08/03/2005

Pages: 4

Conveyance: ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS).

Assignor: HOLT, JOHN M.

Exec Dt: 07/25/2005

Assignee: WARATEK PTY LIMITED

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## Total Assignments: 1

Patent #: NONE

Issue Dt:

Application #: 11111779

Filing Dt: 04/22/2005

Publication #: 20060020913

Pub Dt: 01/26/2006

Inventor: John M. Holt

Title: Multiple computer architecture with synchronization

## Assignment: 1

Reel/Frame: 016608/0494

Recorded: 08/03/2005

Pages: 4

Conveyance: ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS).

Assignor: HOLT, JOHN M.

Exec Dt: 07/25/2005

Assignee: WARATEK PTY LIMITED

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**Total Assignments: 1****Patent #:** NONE**Issue Dt:****Application #:** 11111781**Filing Dt:** 04/22/2005**Publication #:** 20050262513**Pub Dt:** 11/24/2005**Inventor:** John M. Holt**Title:** Modified computer architecture with initialization of objects**Assignment: 1****Reel/Frame:** 016608/0526**Recorded:** 08/03/2005**Pages:** 4**Conveyance:** ASSIGNMENT OF ASSIGNORS INTEREST (SEE DOCUMENT FOR DETAILS).**Assignor:** HOLT, JOHN M.**Exec Dt:** 07/25/2005**Assignee:** WARATEK PTY LIMITEDSUITE 18, 12 TYRON ROAD  
LINDFIELD, NEW SOUTH WALES, AUSTRALIA 2070**Correspondent:** BURTON A. AMERNICKCONNOLLY BOVE LODGE & HUTZ LLP  
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